

REMARKS

Applicants thank the Examiner for indicating that claims 1, 3, 5, 7-14, 16-17, and 19 are allowed. The Examiner also is thanked for indicating that claims 21-23 would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 1, 3, 5, 7-14, 16, 17, and 19-22 are pending in the application. Claims 1, 13, and 20 are independent. By the foregoing Amendment claim 20 has been amended and claim 23 has been canceled. These changes are believed to introduce no new matter and their entry is respectfully requested.

Rejection of Claim 20 Under 35 U.S.C. §102(b)

In the Office Action, the Examiner rejected claim 20 under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 5,146,121 to Searles et al. (hereinafter “*Searles*”). Applicant respectfully traverses the rejection.

A claim is anticipated only if each and every element of the claim is found, either expressly or inherently, in a reference. (MPEP §2131 *citing Verdegaal Bros. v. Union Oil Co. of California*, 814 F.2d 628 (Fed. Cir. 1987)). The identical invention must be shown in as complete detail as is contained in the claim. *Id. citing Richardson v. Suzuki Motor Co.*, 868 F.2d 1226,1236 (Fed. Cir. 1989)).

Claim 20 recites in pertinent part “a device driver to receive a multiphase encoded waveform, having: an amplifier to receive differential signal levels representing the multiphase encoded waveform from the bus and extract the multiphase encoded waveform from the received differential signal levels; and a differential delay-lock loop coupled to stretch the received multiphase encoded waveform timing to a predetermined length, *wherein the differential delay-lock loop includes logic to align rising edges of the received multiphase encoded waveform to rising edges of a transmitted multiphase encoded waveform*” (emphasis added).

In the Office Action, the Examiner stated that *Searles* discloses “a device driver to receive a multiphase encoded waveform (10), having: an amplifier (50, Fig. 2 (50)) to receive differential signal levels (54, 56) representing the multiphase encoded waveform from the bus (20, 22) and extract the multiphase encoded waveform from the received differential signal levels (68); and a differential delay-lock loop (10) coupled to stretch the received multiphase encoded waveform timing to a predetermined length (col. 1, lines 30-36, 44-47, col. 2, lines 13-22, 59-68, col. 4, lines 45-68).” Applicants respectfully disagree.

In the Office Action, the Examiner indicated that claim 23 would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. Although Applicants believe claim 20 is patentable as written, in the interest of expediting prosecution Applicants have amended claim 20 to include the subject matter of claim 23. Accordingly, Applicants respectfully submit that amended claim 20 is in condition for allowance.

CONCLUSION

Applicants submit that all grounds for rejection have been properly traversed, accommodated, or rendered moot, and that the application is in condition for allowance. The Examiner is invited to telephone the undersigned representative if the Examiner believes that an interview might be useful for any reason.

Respectfully submitted,
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Jan. 18, 2007
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